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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yasuyuki MORISHITA

Serial No. 10/689,723

Filed October 22, 2003

ESD PROTECTION CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the cited documents are made of record on the enclosed PTO Form-1449.

As the USPTO has waived the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and patent publication for applications filed after June 30, 2003, copies of the cited U.S. references are not enclosed, as the present application is filed after June 30, 2003. Copies of the cited foreign patent documents and/or non-patent literature are enclosed.

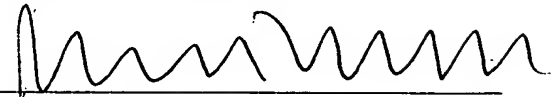
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A concise explanation of the relevance of these items is found on page 2 of the present specification.

Respectfully submitted,

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